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BIB DATA SHEET

CONFIRMATION NO. 3426

SERIAL NUMBER	FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.		
10/577,442	07/13/2006	438	2892	2006_0490A		
RULE						
APPLICANTS Daihei Sugita, Saitama, JAPAN; Masateru Fukuoka, Osaka, JAPAN; Munehiro Hatai, Osaka, JAPAN; Satoshi Hayashi, Osaka, JAPAN; Kazuhiro Shimomura, Saitama, JAPAN; Yoshikazu Kitajima, Saitama, JAPAN; Yasuhiko Oyama, Tokyo, JAPAN;						
** CONTINUING DATA ***** This application is a 371 of PCT/JP04/17955 12/02/2004						
** FOREIGN APPLICATIONS ***** JAPAN 2003-403431 12/02/2003 JAPAN 2004-107584 03/31/2004 JAPAN 2004-282807 09/28/2004 JAPAN 2004-282808 09/28/2004						
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 11/06/2006						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/ERIC W. JONES/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 0	TOTAL CLAIMS 21	INDEPENDENT CLAIMS 2
ADDRESS WENDEROTH, LIND & PONACK, L.L.P. 2033 K STREET N. W. SUITE 800 WASHINGTON, DC 20006-1021 UNITED STATES						
TITLE Method for manufacturing semiconductor chip						
FILING FEE RECEIVED 1080	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		